

FIG. 1

FIG. 2 (a)

BASIC CLOCK

FIG. 2 (b)

DL19

FIG. 2 (c)

DL20

FIG. 2 (d)

DL21

FIG. 2 (e)

DL22

FIG. 2 (f)

DL23

FIG. 2 (g)

DL24

FIG. 2 (h)

DL25

FIG. 2 (i)

DL46

FIG. 2 (j)

DL47

FIG. 2 (k)

DL48

FIG. 2 (l)

DL49

FIG. 2 (m)

DL50

FIG. 2 (n)

DL51

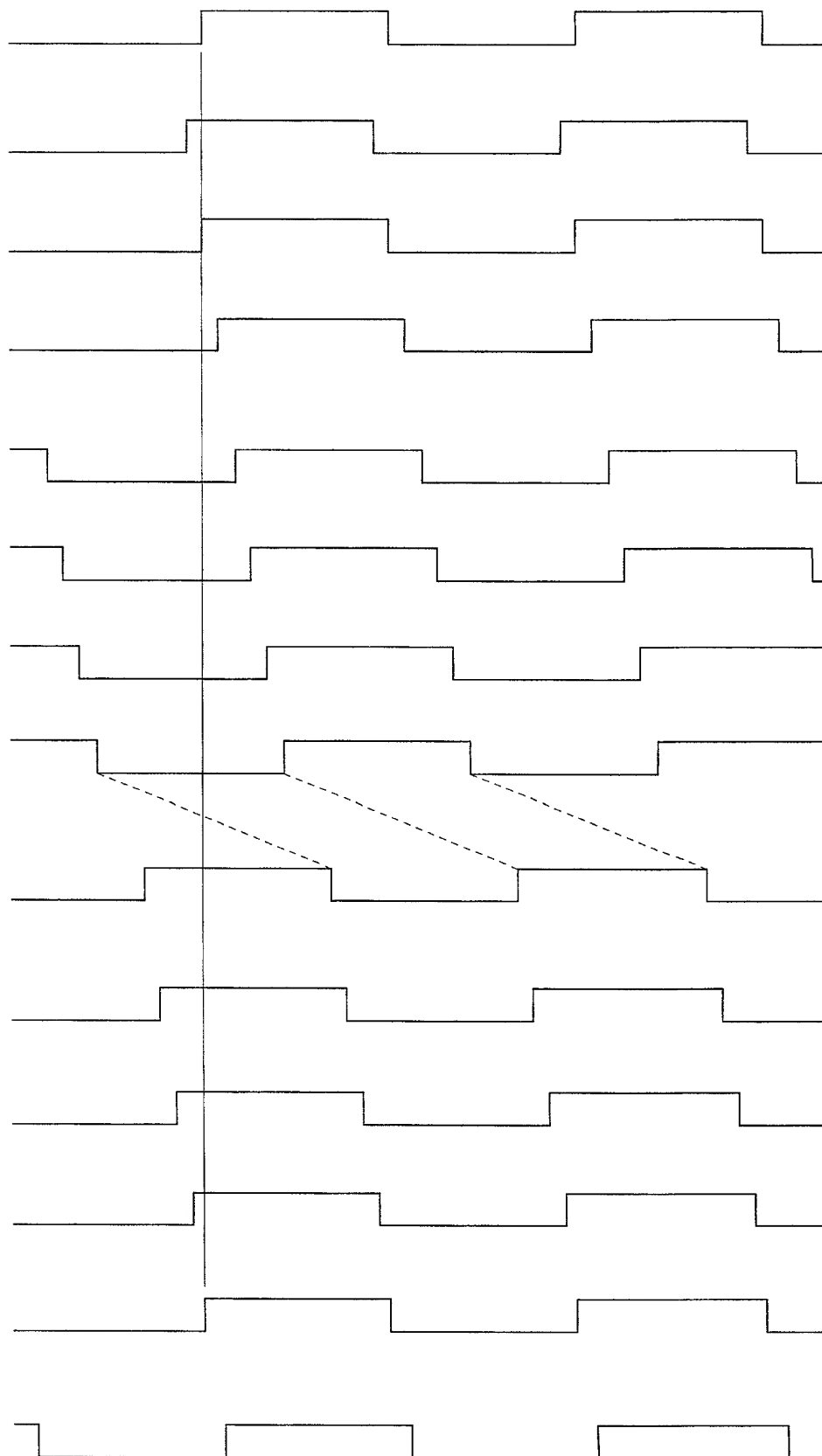


FIG. 3

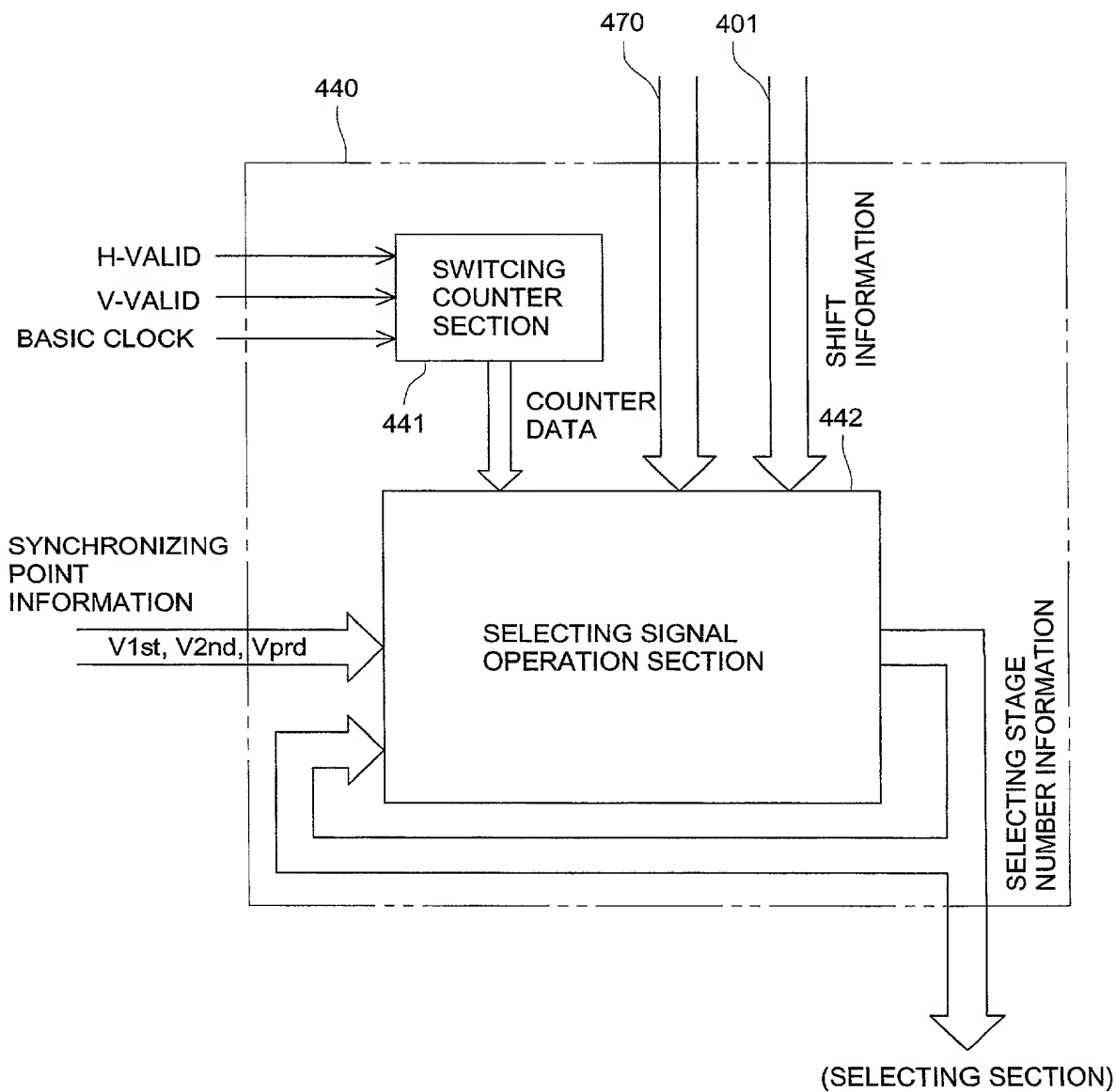


FIG. 4 (a)

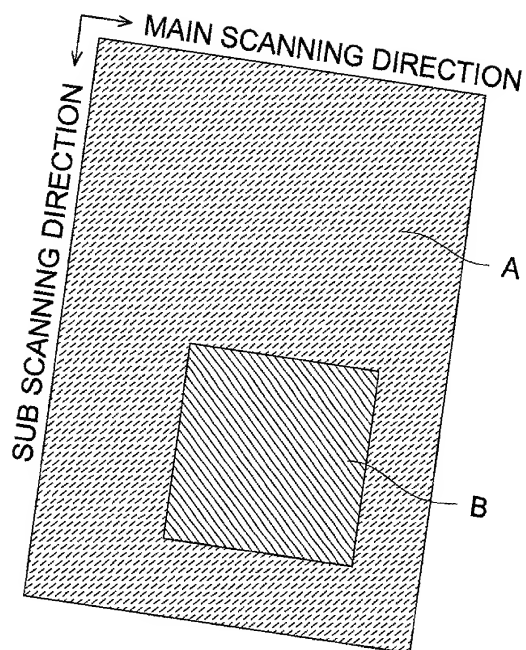
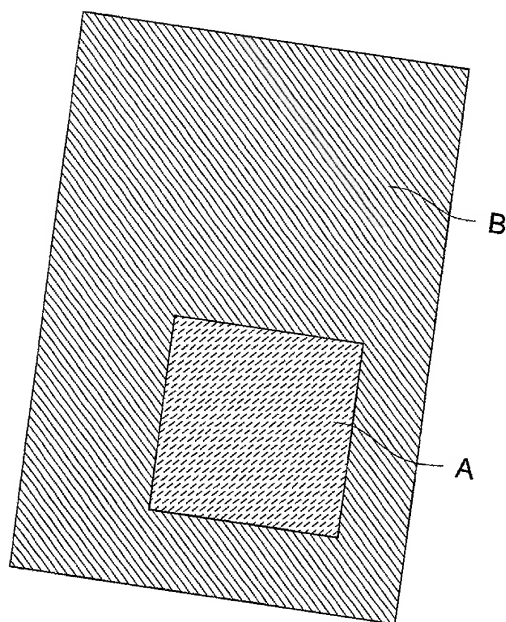


FIG. 4 (b)



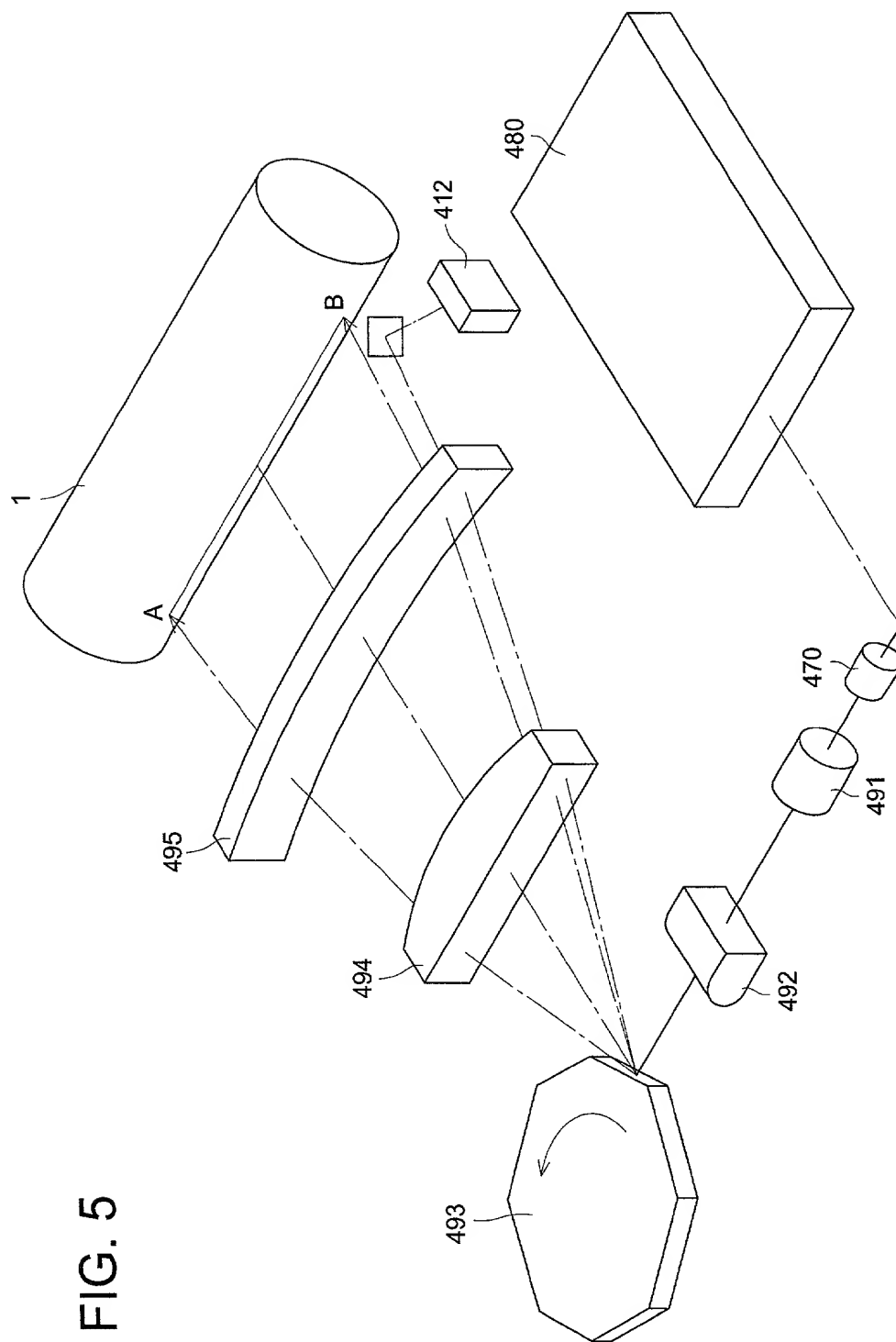


FIG. 5

FIG. 6

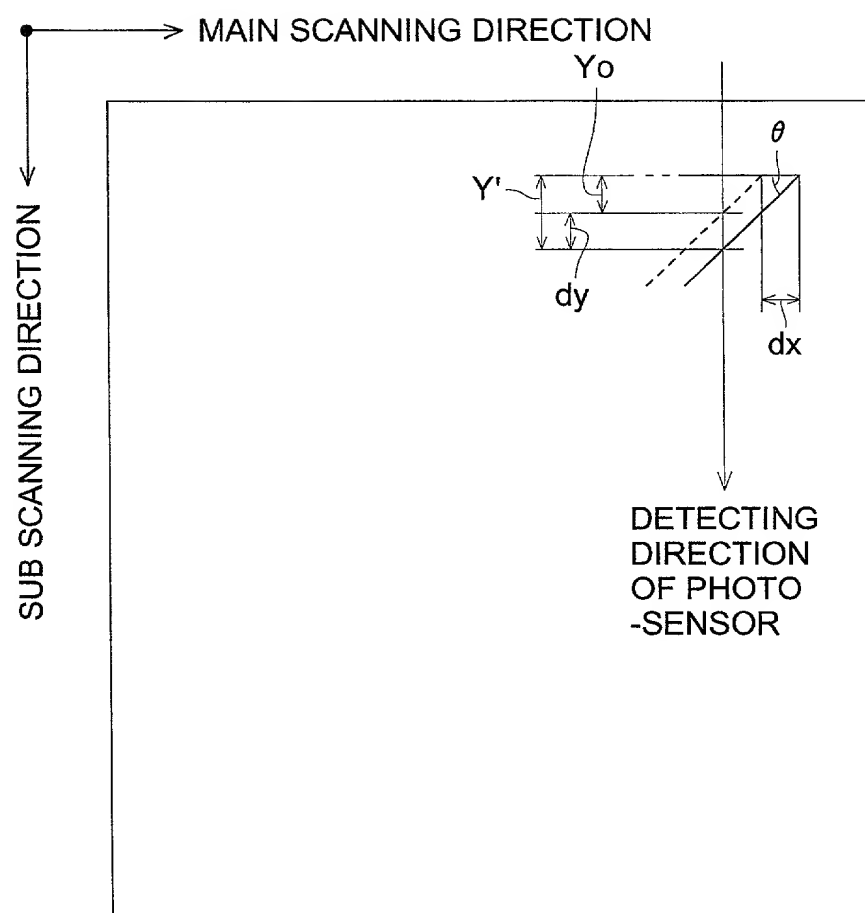


FIG. 7 (a)



FIG. 7 (b)



FIG. 7 (c)



FIG. 7 (d)

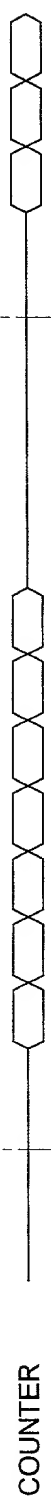


FIG. 7 (e)

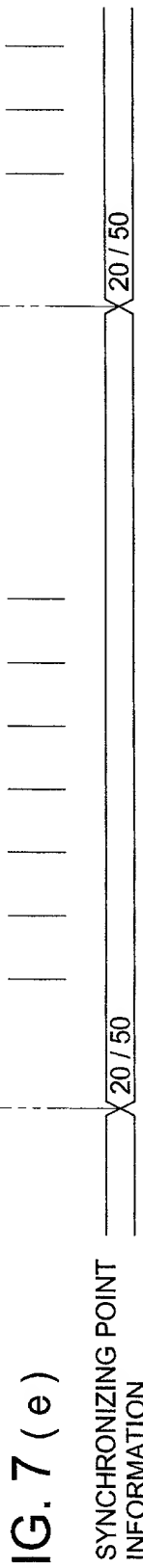


FIG. 7 (f)

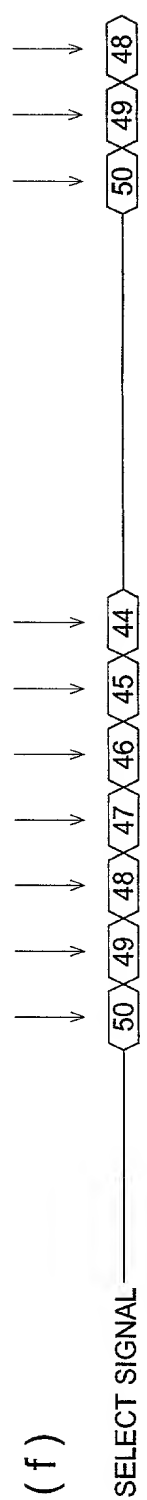


FIG. 7 (g)



FIG. 8 (a)

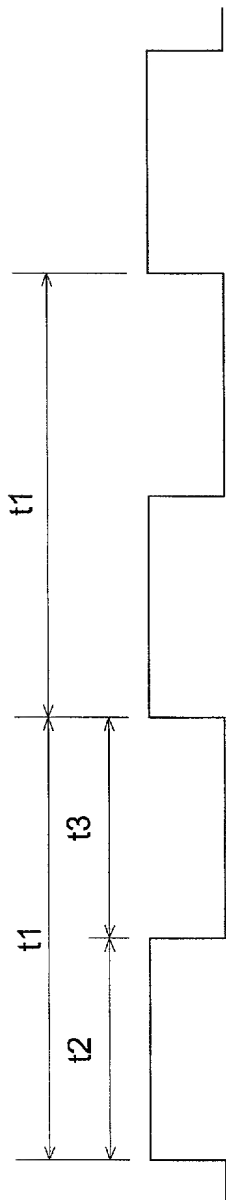
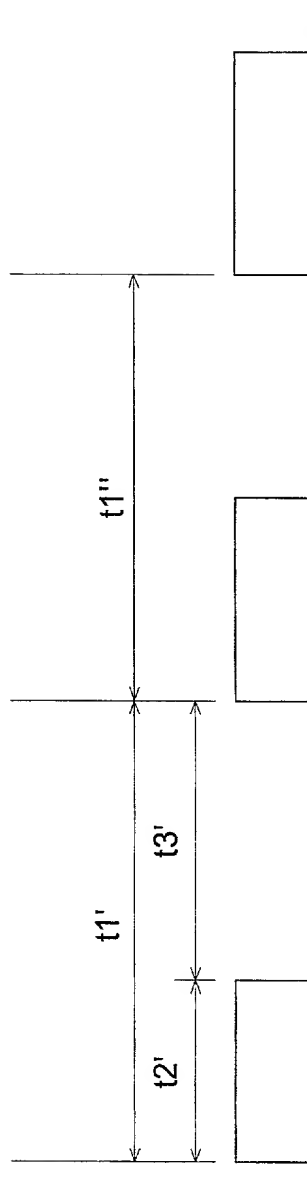
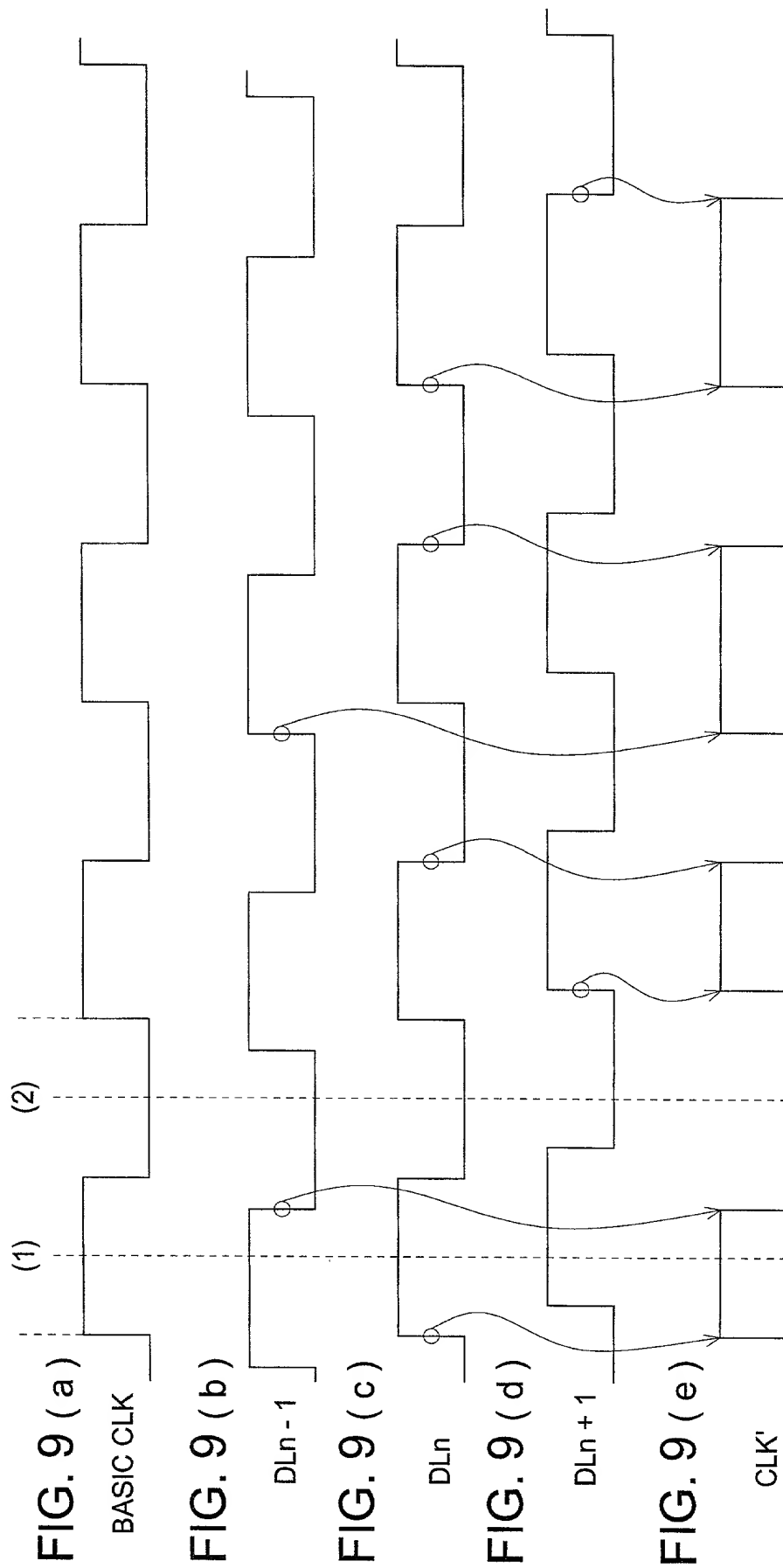
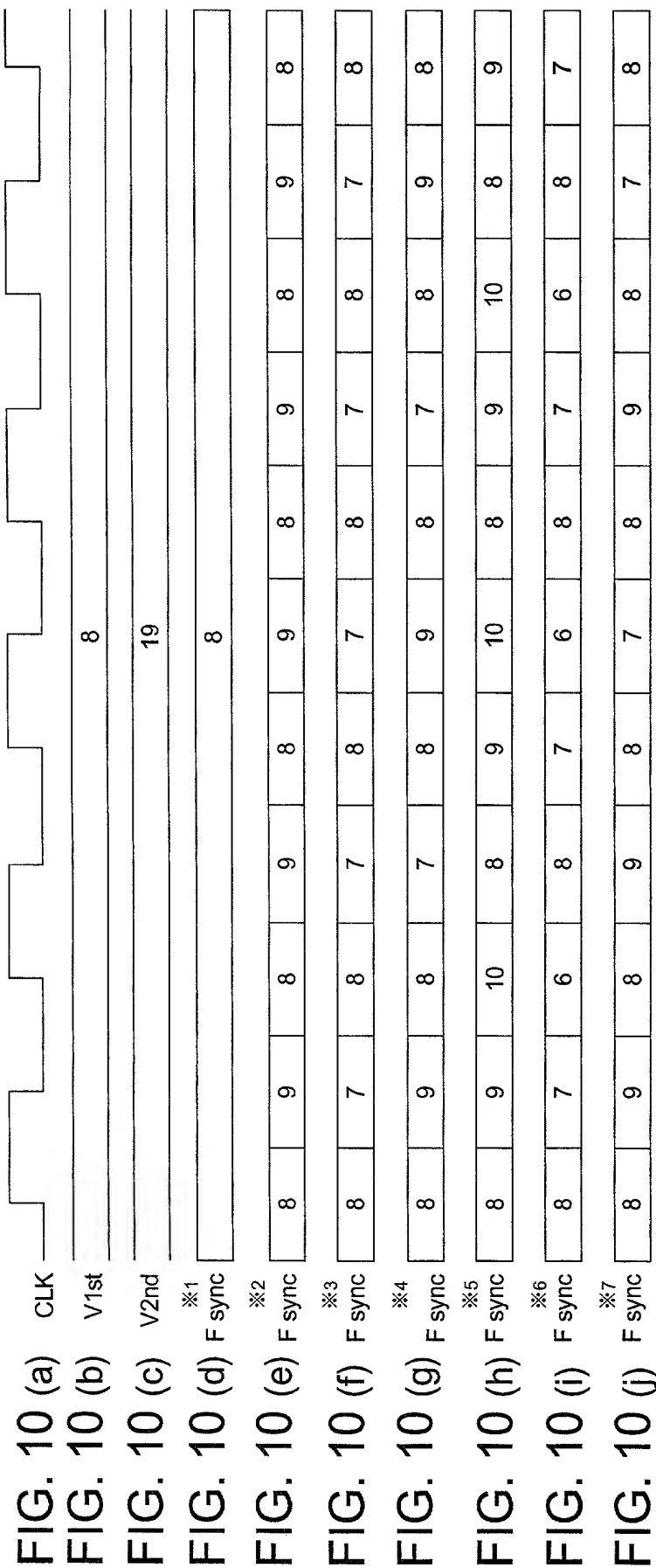


FIG. 8 (b)







1 Fsync IN THE CASE WHERE NO SIGNAL CONTROL IS DONE

2. Fsync in the case where signal control is once done in + direction

※3 Fsync IN THE CASE WHERE SIGNAL CONTROL IS ONCE DONE IN — DIRECTION

4 F_{sync} IN THE CASE WHERE SIGNAL CONTROL IS ONCE DONE IN ± DIRECTION

※5 Fsync IN THE CASE WHERE SIGNAL CONTROL IS TWICE DONE IN + DIRECTION

FSYNC IN THE CASE WHERE SIGNAL CONTROL IS TWICE DONE IN — DIRECTION

7 FSYNC IN THE CASE WHERE SIGNAL CONTROL IS ONCE DONE AT RANDOM

[REVICdata=0]

[REVICEdata=1, TIMESdata=1, MODEdata=00]

[REVICEdata=1, TIMESdata=1, MODEdata=11]

[REVICEdata=1, TIMESdata=1, MODEdata=01]

[REVICedata=1, TIMESdata=2, MODEdata=00]

[REVICdata=1, TIMESdata=2, MODEdata=11]

[REVISEDdata=1, TIMESdata=1, MODEdata=10]

FIG. 11 (a)

FREQUENCY DEVIATION (Δf)

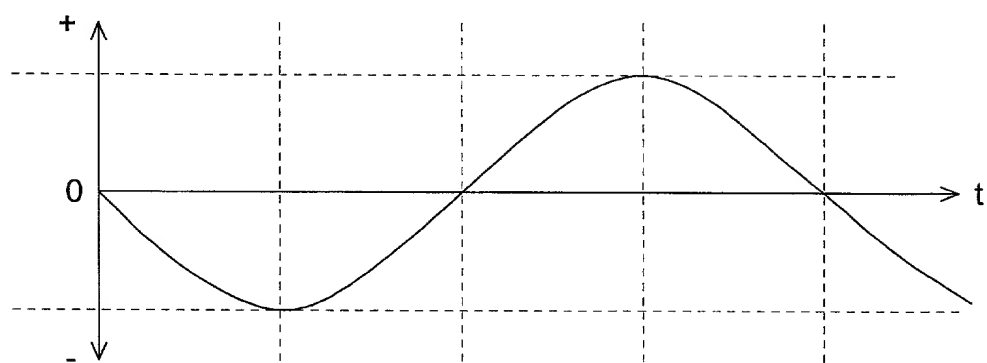


FIG. 11 (b)

FREQUENCY DEVIATION (Δf)

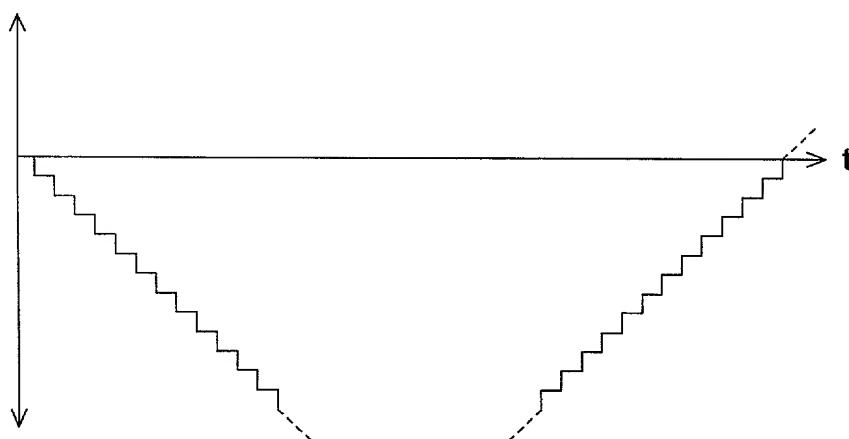
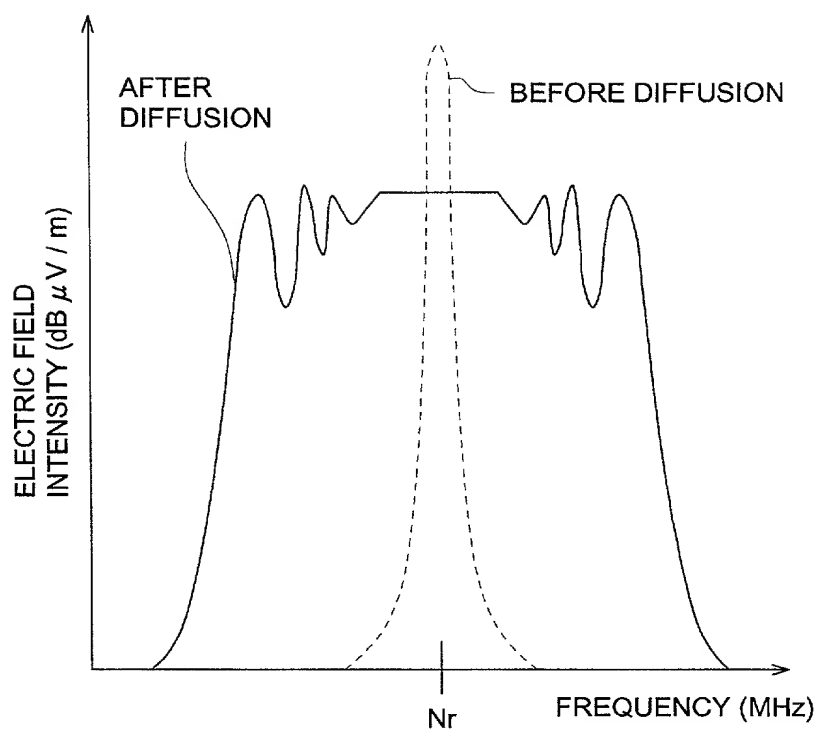


FIG. 12



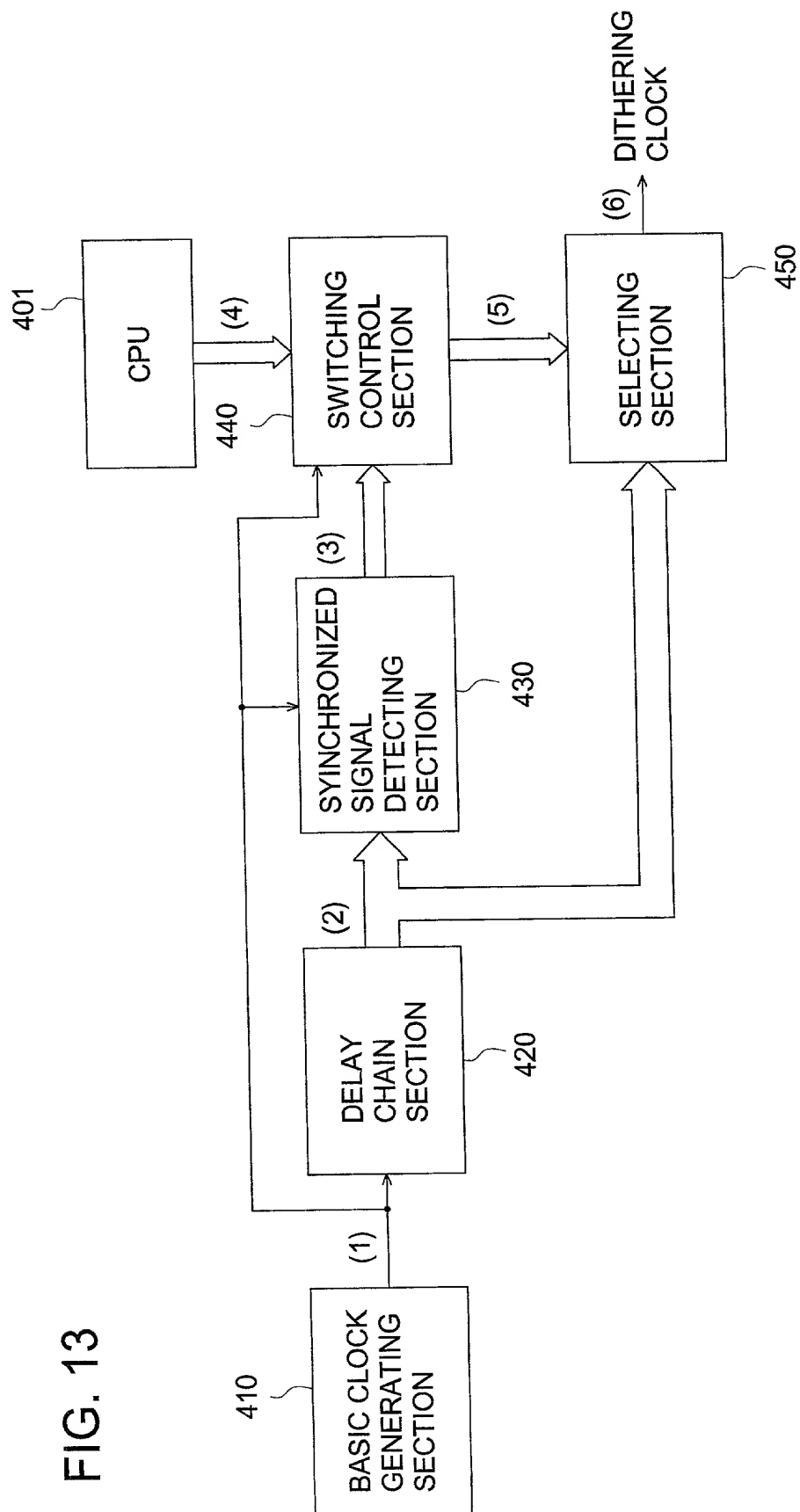


FIG. 14

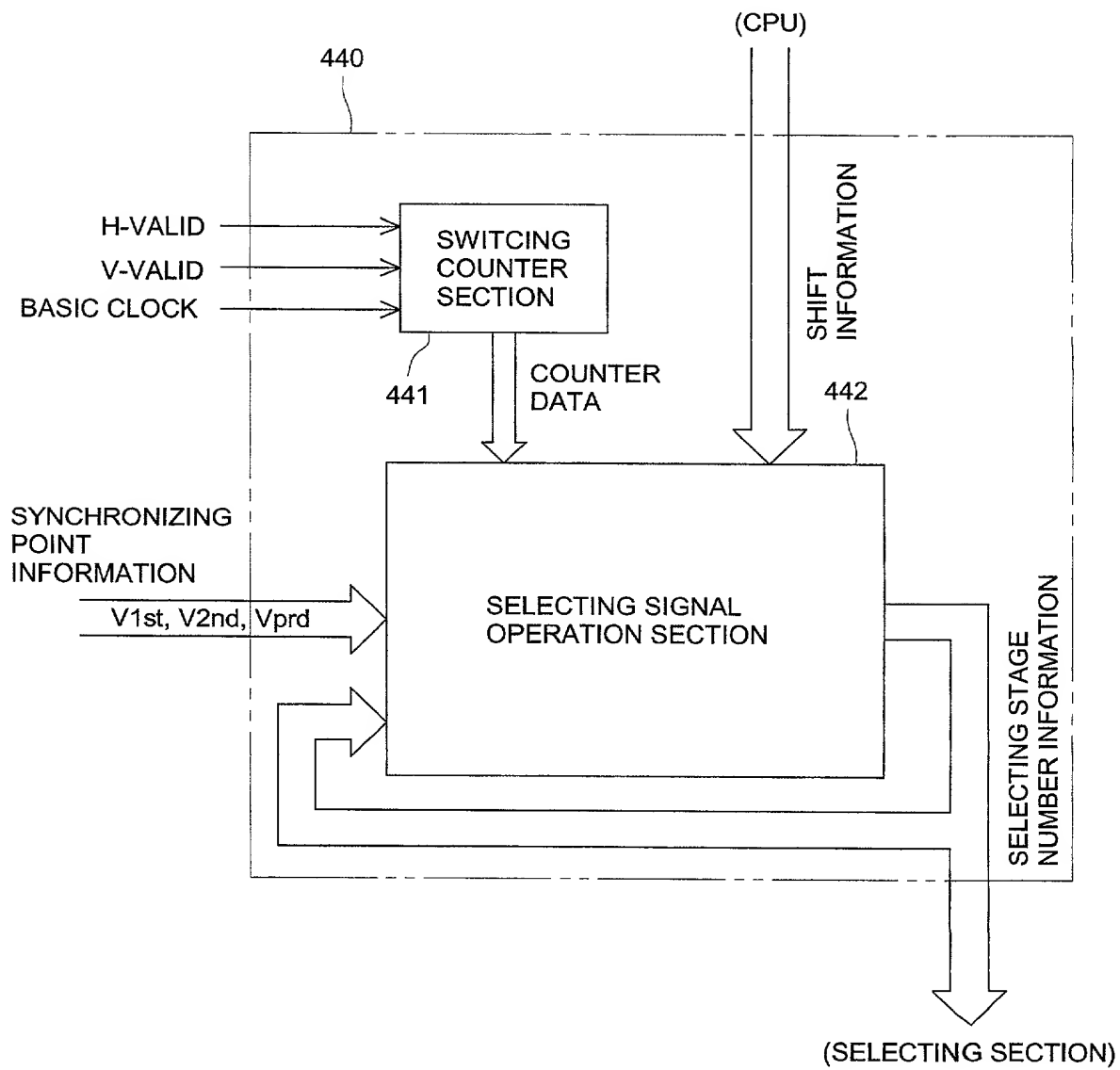


FIG. 16

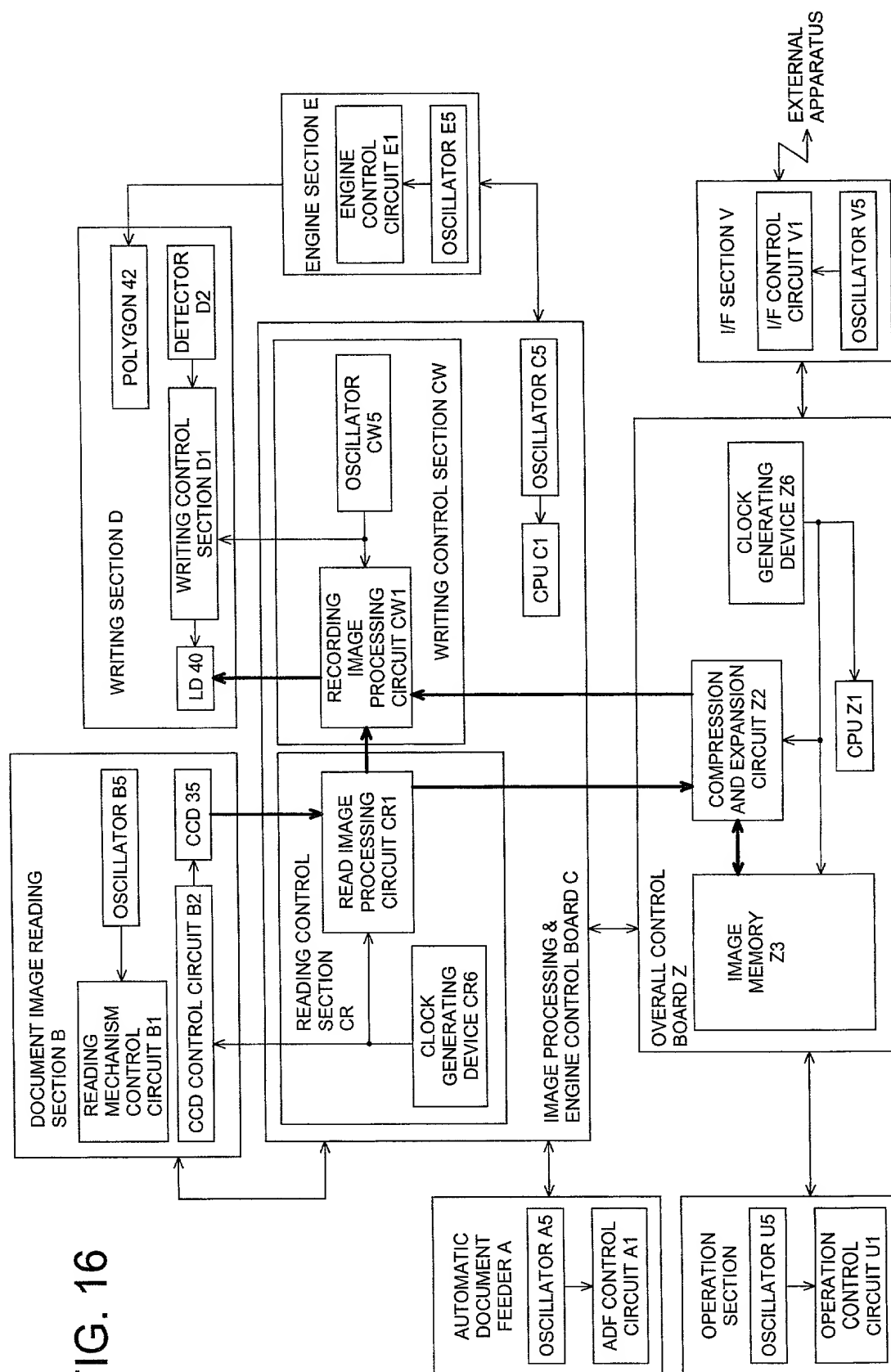


FIG. 17

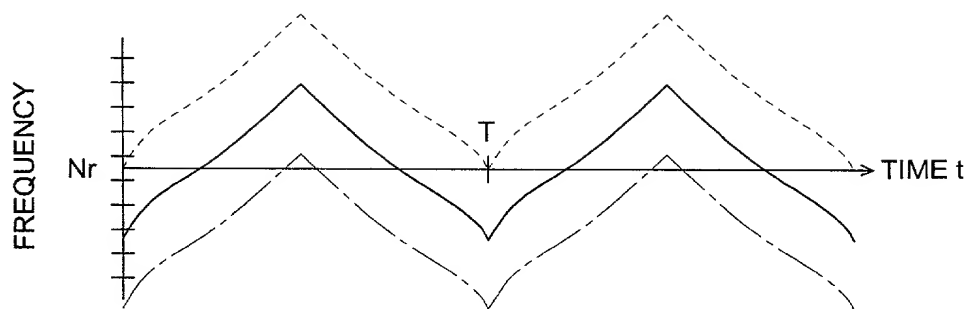


FIG. 18

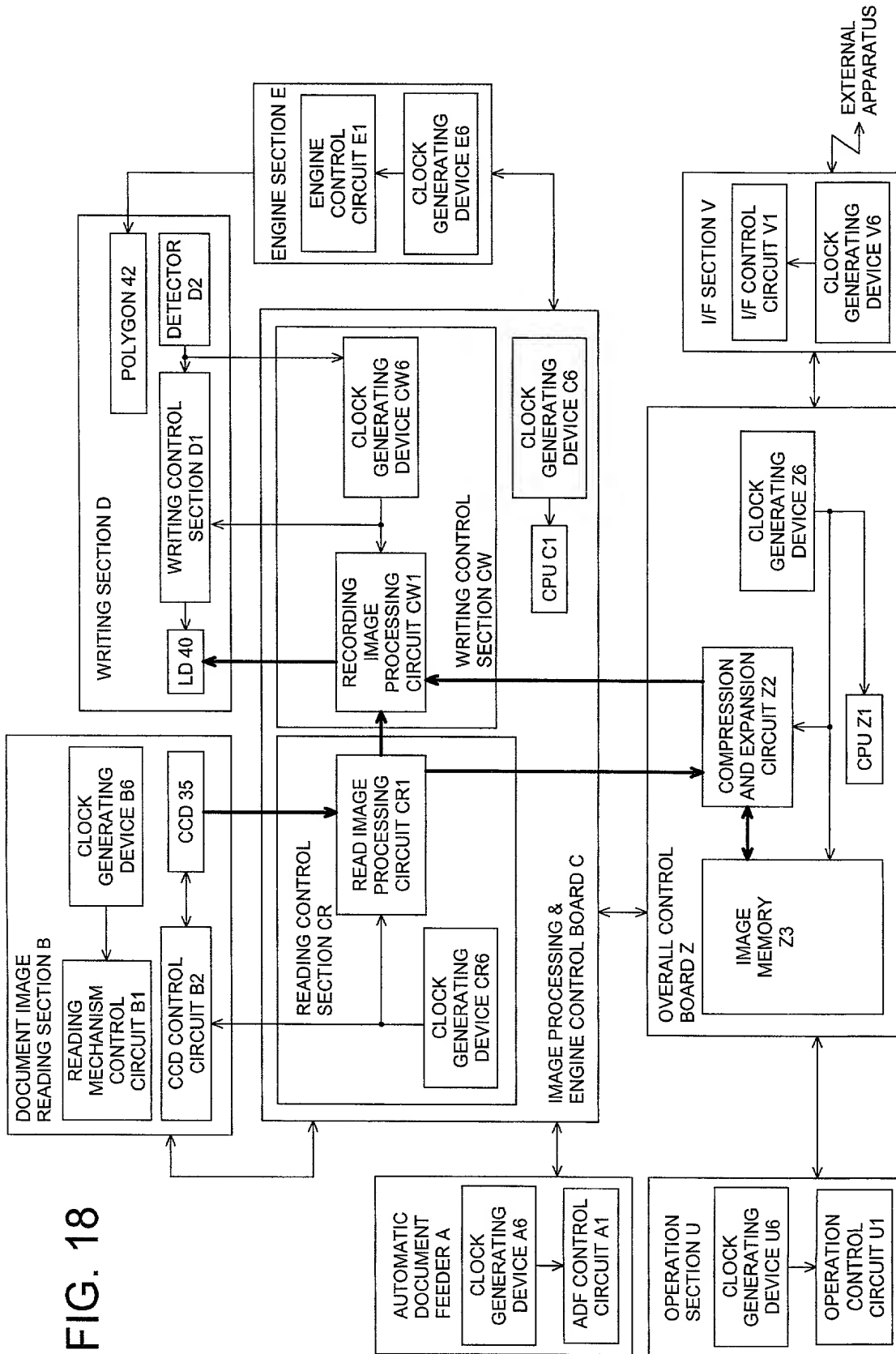


FIG. 19

